IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)	
Hiroshi KOMURASAKI et al.)	Group Art Unit: Unassigned
Application No.: Unassigned)	Examiner: Unassigned
Filed: August 21, 2003)	Confirmation No.: Unassigned
For: OSCILLATOR CIRCUIT AND L LOAD DIFFERENTIAL CIRCUIT)	
ACHIEVING A WIDE OSCILLATION FREQUENCY)	
RANGE AND LOW PHASE NOISE CHARACTERISTICS)	

FIRST INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. § 1.56, Applicants hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Pursuant to 37 C.F.R. §§ 1.98, a copy of each of the documents cited is enclosed.

Japanese Patent Nos. 7-142258, 8-162331, and both publications are discussed in the specification.

The documents are being submitted within three (3) months of the filing or entry of the national stage of this application or before the first Office Action on the merits, whichever is later. Since document is being filed within the time period set forth in 37 C.F.R. § 1.97(b) no fee or statement is required.

Information Disclosure Statement
Application No. <u>Unassigned</u>
Attorney's Docket No. <u>009683-477</u>
Page 2

To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner-initialed copy of this form be returned to the undersigned.

By:

Respectfully submitted,

BORNS, DOANE, SWECKER & MATHIS, L.L.P.

Date: August 21, 2003

Platon N. Mandros Registration No. 22,124

P.O. Box 1404 Alexandria, Virginia 22313-1404 (703) 836-6620

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Substitute for forms 1449A/PTO & 1449B/PTO	ATTORNEY'S DKT No. 009683-477	APPLICATION No. Unassigned			
FIRST INFORMATION DISCLOSURE STATEMENT BY APPLICANT	APPLICANT Hiroshi KOMURASAKI et al.				
	FILING DATE	GROUP			

			I.S. PATENT DOCU	MENIS					
Examiner Initials	Document Number	Kind Code (if known)	Name of Patentee or Applicant of Cited Document				Issue/Publication Date (MM-DD-YYYY)		
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	*7-142258 *8-162331		Jap Jap		06-02-1995 06-21-1996				
Examiner Initials	*Akihiro YAMAGISH RANGE SWITCHING *T. OHNAKADO et a	e name of author k, magazine, jour l et al., "A LOW- G TECHNIQUE," il., "A 1.4dB INSE TRANSISTORS	(in CAPITAL LETTERS); nal, serial, symposium, c publisher, city and/or c OLTAGE 6-GHz-BAND IEICE Trans. Fundament RRION-LOSS, 5GHz TR (DETs) IN 0.18µm CMO p. 162-163.	, title of the artic atalog, etc.), dat ountry where pu CMOS MONOLI als, Vol. E84-A, ANSMIT/RECEI	le (when appro- le, page(s), vol- blished. THIC LC-TANI- No. 2, Februar VE SWITCH U	VCO USING A ry 2001, pp. 559- ITILIZING NOVE	er(s), TUNING 562. L DEPLE	TIO	
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with M.P.E.P. § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

^{*}Also discussed in the specification.